Spartan 6 motherboard

Hardware manual

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Challenge the future

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Chapter 1

Hardware

The system hardware consists of the following components (for the LinoSPAD system) and requirements for operation:

- LinoSPAD motherboard
- LinoSPAD daughterboard with the LinoSPAD chip
- Two power supplies for the LinoSPAD chip and wires to connect
- 5 V DC power jack
- USB3 cable with Micro-B plug
- (Digilent) FPGA Programming Cable

The components are shown in Figure 1.1.



(a) LinoSPAD mainboard.



(b) LinoSPAD daughterboard.



(c) Power supply.

(d) 5 V DC power jack.

(e) Micro-B USB3 cable.

(f) FPGA programming cable.

Figure 1.1: Overview of the hardware components.

The LinoSPAD motherboard is the base component, it is powered through the 5 V DC jack and connected to a host (PC) with the USB3 cable and the FPGA programming cable. A USB3 port is currently required as no backwards compatibility to USB2 has been implemented. The programming cable can be connected to a lower speed USB port.

On top of the motherboard, the daughterboard can be installed. For the chip, additional voltages are required. Therefore two additional power supplies are required to run the system with the LinoSPAD chip.

The motherboard is shown from the top in Figure 1.2, highlighting the different components on the board. The main components are the Xilinx Spartan 6 LX FPGA and the Cypress FX3 USB3 controller. The board fits Spartan 6 LX FPGAs in the FGG676 package. Current configurations use the LX100 and LX150. Furthermore power regulators and memories can be seen. The memories are used to store the firmware for the FPGA and FX3, so they do not have to be reloaded from the host after each power up.



Figure 1.2: Different components on the LinoSPAD mainboard.

The (LX100) FPGA has 15,822 slices in which the logic can be placed, half of the slices contain carrychain structures (for TDCs). 286 16 kB RAM memories are available. There are maximum 480 pins to the outside world, they are connected with the FX3, the connectors for the daughterboard, a FMC interface to a secondary FPGA board and additional pins (as GPIO, SMA etc.).

For the I/O connectors, different voltages can be selected with a jumper: 3.3 V, 2.5 V and 1.8 V are available on board. Other jumpers are implemented to select between different boot modes for the FPGA and FX3. They can be set to load the firmware from the memory or to expect it from the host.

The FX3 is connected through a 32 bit bus with the FPGA, the maximum clock speed is 100 MHz. The maximum achieved speed was found to be around 300 MB/s depending on the host computer and programming.

1.1 Power

The board is powered from a 5V DC supply connected to J1. The four main power rails at 3.3V, 2.5V, 1.8V and 1.2V are generated by two LTM4616 switching regulator modules. The output capacity for each rail is 8A at an average efficiency of 85%. Diode D1 between the two regulator modules turns on when all rails are within +/-10% of their nominal value.

Additional power can be supplied to the daughterboard connected to J20 and J21. 3 of the four GFZ rails are connected to the screw terminal J23. All four GFZ rails for J20 and J21 are decoupled close to the connector with $1 \times 10 \mu F$ and $2 \times 470 nF$ 50V capacitors.

1.2 Configuration memories

Both, the Spartan 6 FPGA and the FX3, have SPI flash memories connected to hold configuration data.

A 16 MBit M25P16 flash memory with SPI interface is connected to the FX3. The FX3 is configured to boot from SPI flash memory by shorting pin pairs 1-3 and 7-8 on J13. The SPI memory is programmed through the FX3 using a firmware programmer previously downloaded to RAM when the FX3 is in bootloader mode after a failed SPI boot or direct USB boot. To get to the bootloader once the SPI flash is programmed USB boot can be selected by shorting pin pairs 1-3 and 5-6 on J13.

A 128 MBit S25FL128S flash memory with quad SPI interface is connected to the Spartan 6. The default mode pin configuration selects SPI master mode therefore only the chip-select has to be connected by placing a jumper on pins 1-2 of J9. Recent versions of the Xilinx iMPACT software support this memory and it can thus be programmed using a JTAG programmer. Diode D2 turns on when the FPGA is configured.

A 64 kBit 24LC64 flash memory with I2C interface is connected to the FMC connector. It can be used to store identifying information to use the card as FMC module. It can be programmed by the FMC carrier or through J17 with power on pin 3 of J18.

1.3 Oscillators

The FX3 uses a 19.2 MHz crystal from which it derives all its clocks. The FX3 can generate an interface clock which is fed to a clock input on the FPGA.

The Spartan 6 is connected to a 48 MHz crystal oscillator on a clock input of bank 0. It runs from 1.8 V to 3.3 V and can be used to generate other frequencies through the PLLs in the FPGA. J7, J8, J10 and J11 connect to clock capable inputs on the FPGA and can be used for single-ended or differential clocks to the FPGA.

1.4 Connectors and jumpers



Figure 1.3: Connectors on the LinoSPAD mainboard. Non-keyed connectors have pin 1 marked red.

ID	Name	Description				
J1	DC power	Center positive 5V DC supply. (power depending gested 20W)	g on a	ppli	catio	on; sug-
			GND	1	2	2V5
			GND	3	4	S6_TMS
10			GND	5	6	S6_TCK
J 2	Xilinx JTAG	Standard pinout.	GND	7	8	S6_TDO
			GND	9	10	S6_TDI
			GND	11	12	NC
			GND	13	14	NC

ID	Name	Description						
				3V3	1	1	2	VCCO
J3	FPGA Bank 0	Supplies J10, J11, J12 and J19		2V5 1V8		3	4	VCCO
			FMC_	VADJ	1	7	8	VCCO
14	EDCA Book 1 & 5	I/O voltage for I20		3V3	1	1	2	VCCO
J4	I'r OA Dallk 1 & J	1/O voltage for 520		2V5 1V8		3	4	VCCO
				3V3	1	1	2	vcco
J5	FPGA Bank 3 & 4	I/O voltage for J21		2V5	1	3	4	VCCO
			PW	1V8	J	5	6	VCCO GND
16	FPGA startup	Power control and configuration override	HS	WAPEN	1	3	4	GND
00	r i on stantap			M)	5	6	GND
17	SMA 2W5 D	Positive clock input on EPGA 2.5V I/O		М		7	8	2V5
		Toshive clock liput on FTGA 2.5V 1/O						
<u> </u>	SMA 2V5 N	Negative clock input on FPGA 2.5V I/O						
			CSO_B MISO_0	1	2		CSN AISO	2
J9	FPGA program	Miscellaneous programming pins	MISO_1	5	6	N	IISO_	3
			CCLK	7	8	Р	ROG	RAM_B
110			GND	9	10	2	V5	
<u>J10</u>	SMA FMC P	Positive clock input on FPGA Bank 0 I/O						
J11	SMA FMC N	Negative clock input on FPGA Bank 0 1/0						
J12	FPGA GPIO	General purpose FPGA I/O on Bank 0						
			PMODE_	0 1		2	GN	1D
J13	FX3 startup	FX3 configuration mode selector	2v 2v	5 5		6	PN	10DE_1
			PMODE_	2 7		8	GN	ND
T14	Miana USD2	Minne USD2 Device composter	2V	5 9		10	CL	KIN
J14	MICTO USB3	Micro USB3 Device connector	EV2 T	DI	1		Ľ	
			FX3_1 FX3_TI		3	4	12	2C_SDA 2C_SCL
J15	FX3 debug	Miscellaneous pins for debugging	FX3_TRS	ΓN	5	6	G	PIO_50
			FX3_TM	AC	7	8	G	PIO_51
			SCL	1	2	10	FMC_	_TCK
117	EMC dabug	Connection to EMC EEDDOM and ITAC	SDA	3	4	1	FMC_	TDI
J1/	rme debug	Connection to FMC EEPROM and JTAG	GA1	5	6	1	FMC	TDO TMS
			GND	9	10	1	FMC_	TRSTN
119	FMC nowor	Access to EMC power rails	FMC_3V	3 1		2	FM	C_3V3
J 10	Twic power	Access to Fille power fails _	FMC_3V3AU2 GNI	X 3 D 5		4	FM0	C_VADJ C_12V0
J19	FMC module	FMC module connector: low pin count version	n [·] CLK0) CI	K	1 ai	nd o	differ-
		ential pairs are connected: LX45 only 12 pairs	s. LX75	only	1	7 n:	airs	
120	Left GEZ	10 x 40 GEZ connector: 4 supply rails and up	to 160 si	nole		nde	ed s	ionals
121	Pight GE7	10 x 40 GEZ connector: 4 supply rails and up	to 160 si	ngle		nde	d o	ignals
J21	Kight OFZ	10 x 40 GFZ connector, 4 suppry rans and up	100 81	3V3	1		u s	JEZ VO
J22	GFZ_V0	Rail 0 for GFZ	_	2V5	3	4	(GFZ_V0
				1V8	5	6	(GFZ_V0
						-1	(JFZ_V1 GND
J23	GFZ power	Connection to GFZ rails 1-3				3		GFZ_V2
	Poor					4	(GND
						5 6		JFZ_V3 GND
104	CEZ VI	Doil 1 for CEZ		1V2	1	2	(GFZ_V1
J24	ULT 1	Kall I IOF GFZ	EMC	5V0	3	4	(GFZ_V1
		1	FMC_I	1210	3	1 0	1	JI Z_ V I

1.4.1 GFZ daughtercard connectors

These connectors are to be used with Samtec[®] 10x40 GFZ spring connector arrays. The standard height of the connectors and therefore spacing between the PCBs is 3mm. The pinout is defined in figure Figure 1.4.



Figure 1.4: GFZ connector pinout.

1.5 Mechanical specifications

1.5.1 Motherboard

The motherboard is a standard FR4 PCB with 2mm thickness as outlined in figure Figure 1.5.

1.5.2 LinoSPAD daughterboard

The LinoSPAD PCB is 80mm x 70mm and fits to the top part of the motherboard. It is a PCB with 0.8mm to 1.2mm thickness and outline shown in figure Figure 1.7. Due to it being flexible it should be fixed with an additional plate on top or spacers between the PCBs to ensure good connections. The PCB is fixed to the motherboard using twelve M2.5 screws.



Figure 1.5: Motherboard mechanical drawing. The motherboard has four mounting holes in the corners. Additional dimensions for the daugherboard fixation are included in the daughterboard drawing.



Figure 1.6: Motherboard pin header positions. Pin pitch is 2mm except for the screw terminal J23 where it is 3.5mm.



Figure 1.7: Daughterboard mechanical drawing.

1.6 Master user constraint file (.ucf)

This listing contains timing constraints and pin locations for all fixed Spartan 6 FPGA connections:

```
#Spartan 6 LX45-150 PCB rev. 13.09
*****
#Bank 2 is fixed at 2.5V
CONFIG VCCAUX = 2.5;
#Banks 0,1,3,4,5 are selectable 1.8V - 3.3V
#Bank 0 is FMC, Osc and GPIO
#Bank 2 is FX3 and configuration
#Banks 1,5 are GFZ left
#Banks 3,4 are GFZ right
 #Clock sources
.
#Bank 0
NET "OSC_48MHZ" LOC = C14;
NET "OSC_48MHZ" INM_NET = "osc48";
TIMESPEC "TS_osc48" = PERIOD "osc48" 48 MHz HIGH 50 % INPUT_JITTER 80 ps;
NET "SMA_FMC_P" LOC = C15; #J10, bottom right
NET "SMA_FMC_N" LOC = A15; #J11, bottom left
#Bank 2
NET "SMA_2V5_P" LOC = AE13 | IOSTANDARD = LVCMOS25; #J7, top right
NET "SMA_2V5_N" LOC = AF13 | IOSTANDARD = LVCMOS25; #J8, top left
#FX3 (Bank 2)
NET "FX3*" IOSTANDARD = LVCMOS25 | DRIVE = 8 | FAST;
NET "FX3_PCLK" TNM_NET = "pclk100";
TIMESPEC "TS_pclk100" = PERIOD "pclk100" 100.8 MHz HIGH 50 %;
NET "FX3*" TNM =
                                 "FX3 TIMING";
TIMEGRP "FX3_TIMING" OFFSET = OUT 7 ns AFTER "FX3_PCLK" RISING VALID 3.5 ns;
TIMEGRP "FX3_TIMING" OFFSET = IN 2 ns BEFORE "FX3_PCLK" RISING VALID 4 ns;
NET "FX3_PCLK" LOC = AD12;
NET "FX3_RESET_N" LOC = AD8;
NET "FX3_SLCS_N" LOC = AA15;
NET "FX3_SLOE_N" LOC = AE21;
NET "FX3_SLOE_N" LOC = AD18;
NET "FX3_SLWR_N" LOC = AD18;
NET "FX3_SLWR_N" LOC = AB15;
NET "FX3_PKTEND_N" LOC = AF19;
NET "FX3 FLAG A" LOC = AD10;
NET "FX3_FLAG_B" LOC = AD10;
NET "FX3_FLAG_B" LOC = AE15;
NET "FX3_FLAG_C" LOC = AC15;
NET "FX3_FLAG_D" LOC = AD15;
NET "FX3_FIFOADR[0]" LOC = AB17;
NET "FX3_FIFOADR[1]" LOC = AF16;
NET "FX3_GPIO[0]" LOC = AF15; #GPIO[26]
NET "FX3_GPIO[1]" LOC = AC17; #GPIO[27]
#NET "FX3_CTL[15]" LOC = AC14;
NET "FX3_DQ[0]" LOC = AE9;
NET "FX3_DQ[1]" LOC = AF18;
NET "FX3_DQ[2]" LOC = AF19;
NET "FX3_DQ[2]" LOC = AF11;
NET "FX3_DQ[4]" LOC = AF11;
NET "FX3_DQ[5]" LOC = AF12;
NET "FX3_DQ[6]" LOC = AF12;
NET "FX3_DQ[7]" LOC = AE19;
NET "FX3_DQ[8]" LOC = AC12;
NET "FX3_DQ[9]" LOC = W12;
NEI "FX_DQ[9]" LOC = W12;
NET "FX3_DQ[10]" LOC = AB13;
NET "FX3_DQ[11]" LOC = V12;
NET "FX3_DQ[12]" LOC = AA13;
NET "FX3_DQ[13]" LOC = AR15;
NET "FX3_DQ[14]" LOC = AF21;
NET "FX3_DQ[14]" LOC = AF21;
NET "FX3_DQ[15]" LOC = AF10;
#Not connected on LX75
#Not connected on LX75
NET "FX3_DQ[16]" LOC = AB18;
NET "FX3_DQ[17]" LOC = AA18;
NET "FX3_DQ[19]" LOC = A019;
NET "FX3_DQ[20]" LOC = A017;
NET "FX3_DQ[20]" LOC = A017;
NET "FX3_DQ[21]" LOC = AA19;
NET "FX3_DQ[23]" LOC = AA216;
NET "FX3_DQ[23]" LOC = AA16;
NET "FX3_DQ[24]" LOC = AC16;
NET "FX3_DQ[25]" LOC = AD21;
NET "FX3_DQ[26]" LOC = Y18;
 NET "FX3_DQ[27]" LOC = V16;
NET "FX3 DO[28]" LOC = Y16;
NET "FX3_DQ[29]" LOC = W17;
NET "FX3_DQ[30]" LOC = V15;
NET "FX3_DQ[31]" LOC = W16;
```

#SPI Flash #-----

NET "SPI*" IOSTANDARD = LVCMOS25 | DRIVE = 8 | FAST; NEI "SPI-" IDIANDAND = LOURDS25 | DI NET "SPI_CSN" LOC = AF4; NET "SPI_SCK" LOC = AP22; NET "SPI_MOSI" LOC = AP20; NET "SPI_HISO" LOC = AD20; NET "SPI_HOLDN" LOC = AP17 | PULLUP; NET "SPI_HOLDN" LOC = AP17 | PULLUP; #GPIO (Bank 0) #-----#13.09: GPI0[0;1] not connected on LX45,75
NET "GPI0[0]" LOC = H17;
NET "GPI0[1]" LOC = J17;
NET "GPI0[2]" LOC = A18; NEI "GPI0[3]" LOC = A18; NET "GPI0[3]" LOC = B18; NET "GPI0[4]" LOC = B18; NET "GPI0[5]" LOC = A19; NET "GPI0[5]" LOC = C19; NET "GPI0[6]" LOC = A20; NET "GPI0[7]" LOC = B20; NET "GPI0[8]" LOC = A21; NET "GPIO[9]" LOC = C21; #GFZ left (Bank 1,5) #56-#73 not connected on LX45 #GFZ left (Bank 1,5) #56-#73
#
Term "GFZ_LP[1]" LOC = P21;
NET "GFZ_LP[2]" LOC = P24;
NET "GFZ_LP[3]" LOC = D24;
NET "GFZ_LP[3]" LOC = D24;
NET "GFZ_LP[5]" LOC = D25;
NET "GFZ_LP[5]" LOC = D25;
NET "GFZ_LP[1]" LOC = M18;
NET "GFZ_LP[7]" LOC = M18;
NET "GFZ_LP[1]" LOC = T23;
NET "GFZ_LP[11]" LOC = T23;
NET "GFZ_LP[11]" LOC = V18;
NET "GFZ_LP[14]" LOC = V18;
NET "GFZ_LP[14]" LOC = V18;
NET "GFZ_LP[16]" LOC = N24;
NET "GFZ_LP[18]" LOC = V24;
NET "GFZ_LP[21]" LOC = N25;
NET "GFZ_LP[21]" LOC = N25;
NET "GFZ_LP[21]" LOC = N25;
NET "GFZ_LP[21]" LOC = V24;
NET "GFZ_LP[21]" LOC = N25;
NET "GFZ_LP[21]" LOC = N26;
NE NET "GFZ_LP[23]" LOC = Y24; NET "GFZ_LP[24]" LOC = AA25; NET "GFZ_LP[25]" LOC = X20; NET "GFZ_LP[26]" LOC = V22; NET "GFZ_LP[27]" LOC = A223; NET "GFZ_LP[28]" LOC = A224; NET "GFZ_LP[30]" LOC = AB24; NET "GFZ_LP[31]" LOC = AB23; NET "GFZ_LP[32]" LOC = H234; NET "GFZ_LP[32]" LOC = A223; NET "GFZ_LP[64]" LOC = H22; NET "GFZ_LP[65]" LOC = G20; NET "GFZ_LP[65]" LOC = G20; NET "GFZ_LP[67]" LOC = L20; NET "GFZ_LP[68]" LOC = K22; NET "GFZ_LP[69]" LOC = K22; NET "GFZ_LP[70]" LOC = AB21; NET "GFZ_LP[71]" LOC = H19; NET "GFZ_LP[72]" LOC = P17; NET "GFZ_LP[73]" LOC = R17;

NET "GFZ_LN[1]" LOC = P22;

NET	CER IN[2] LOG D2C.
14151	"GFZ_LN[2]" LOC = $D26$;
	GFZ_LN[5] LOC - A25;
NEI	"GFZ_LN[4]" LOC = C26;
NEI	"GFZ_LN[5]" LOC = B26;
NET	"GFZ_LN[6]" LOC = N18;
NET	"GFZ_LN[7]" LOC = N19;
NET	"GFZ_LN[8]" LOC = R19;
NET	"GFZ_LN[9]" LOC = T19;
NET	"GFZ_LN[10]" LOC = A23;
NET	"GFZ_LN[11]" LOC = U24;
NET	"GFZ_LN[12]" LOC = R24;
NET	"GFZ_LN[13]" LOC = V17;
NET	"GFZ_LN[14]" LOC = V19;
NET	"GFZ_LN[15] " LOC = N24;
NET	"GFZ_LN[16] " LOC = P26;
NET	"GFZ LN[17] " LOC = W26;
NET	"GFZ LN[18]" LOC = V26;
NET	"GFZ LN[19]" LOC = R26;
NET	"GFZ LN[20]" LOC = N21:
NET	"GFZ $LN[21]$ " $LOC = W24$:
NET	"GE7_LN[22]" LOC = T26:
NET	"GF7_LN[23]" LOC = V26:
NET	UCEZ IN[24] LOC - 3026.
NET	GFZ_IN[24] LOC - MA20,
NEI	"GF2_LN[25]" LOC = 121;
NEI	"GF2_LN[26]" LOC = W22;
NEI	"GF2_LN[27]" LOC = AC24;
NEI	"GF2_LN[28]" LOC = 023;
NEI	"GFZ_LN[29]" LOC = AC26;
NET	"GFZ_LN[30]" LOC = AB26;
NET	"GFZ_LN[31]" LOC = AFZ4;
NET	"GFZ_LN[32]" LOC = U22;
NET	"GFZ_LN[33]" LOC = U20;
NET	"GFZ_LN[34]" LOC = AE26;
NET	"GFZ_LN[35]" LOC = AD26;
NET	"GFZ_LN[36]" LOC = V20;
NET	"GFZ_LN[37]" LOC = W19;
NET	"GFZ_LN[38]" LOC = AA24;
NET	"GFZ_LN[39]" LOC = AF25;
NET	"GFZ_LN[40]" LOC = AA22;
NET	"GFZ_LN[41]" LOC = M21;
NET	"GFZ_LN[42]" LOC = L18;
NEI	"GFZ_LN[43]" LOC = K19;
NEI	"GFZ_LN[44]" LOC = NZ3;
NET	"GFZ_LN[45]" LOC = N26;
NEI	"GFZ_LN[46]" LOC = J26;
NET	$"GF2_LN[47]" LOC = G26;$
NET	GFZ_LN[40] LOC - H20;
NET	GFZ_LN[49] LOC - M20;
NET	GFZ_LN[50] LOC - L24; "CFZ_IN[51] LOC - F26.
NET	GFZ_LN[51] LOC - E26;
NET	GFZ_LN[JZ] LOC - FZO; "CFZ IN[53]" IOC - K19;
NET	"CFZ_IN[53] LOC - 126.
NET	"CFZ_IN[54] LOC - M26.
NET	$GFZ_IN[55] = LOC = C19$
NET	"CFZ_IN[50] LOC - C24:
14151	Gra_ma[37] moc = Gz4,
NFT	"CF7 IN[58]" IOC = F24
NET	"GFZ_LN[58]" LOC = E24; "CFZ_LN[59]" LOC = D22:
NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "CFZ_LN[60]" LOC = C24;
NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = C24;
NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = U26;
NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = J24; "GFZ_LN[63]" LOC = J24;
NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[62]" LOC = J24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[64]" LOC = G22;
NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[62]" LOC = U26; "GFZ_LN[63]" LOC = H21; "GFZ_LN[64]" LOC = G22; "GFZ_LN[65]" LOC = G21; "GFZ_LN[65]" LOC = G2
NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = J24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[65]" LOC = G22; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[65]" LOC = K2
NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = J24; "GFZ_LN[62]" LOC = J24; "GFZ_LN[61]" LOC = G22; "GFZ_LN[65]" LOC = G21; "GFZ_LN[66]" LOC = G21; "GFZ_LN[61]" LOC = K21; "GFZ_LN[61]" LOC = L21;
NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = D24; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = H21; "GF2_LN[65]" LOC = G21; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = L21; "GF2_LN[68]" LOC = L22;
NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[64]" LOC = G22; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[67]" LOC = L21; "GFZ_LN[69]" LOC = J22; "GFZ_LN[69]" LOC = B21: "GFZ_LN[69]" LOC = B21:
NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = C24; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = J24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[63]" LOC = H21; "GFZ_LN[65]" LOC = G22; "GFZ_LN[65]" LOC = G21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[68]" LOC = L21; "GFZ_LN[68]" LOC = H21; "GFZ_LN[68]" LOC = H21; "GFZ_LN[68]" LOC = H21; "GFZ_LN[68]" LOC = H21; "GFZ_LN[68]" LOC = H22; "GFZ_LN[68]" LOC = H22; "GFZ_LN[68]" LOC = H22; "GFZ_LN[68]" LOC = H22; "GFZ_LN[68]" LOC = H22; "GFZ_LN[69]" LOC = H22; "GFZ_LN[69]" LOC = H22; "GFZ_LN[69]" LOC = H22; "GFZ_LN[69]" LOC = H22; "GFZ_LN[60]" LOC = H2
NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = U26; "GFZ_LN[63]" LOC = H21; "GFZ_LN[63]" LOC = G21; "GFZ_LN[65]" LOC = G21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[69]" LOC = R21; "GFZ_LN[69]" LOC = R21; "GFZ_LN[61]" LOC = R21; "GFZ_LN[61]" LOC = R21; "GFZ_LN[70]" LOC = R22; "GFZ_LN[70]" LOC = R2]; "GFZ_LN[70]" LOC = R2
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = D24; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = H21; "GF2_LN[66]" LOC = G22; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = AB22; "GF2_LN[70]" LOC = AB22; "GF2_LN[70]" LOC = J20; "GF2_LN[70]" LOC = D18; "GF2_LN[70]" LOC =
NET NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = H21; "GFZ_LN[63]" LOC = H21; "GFZ_LN[65]" LOC = G22; "GFZ_LN[66]" LOC = C21; "GFZ_LN[61]" LOC = L21; "GFZ_LN[61]" LOC = J22; "GFZ_LN[61]" LOC = A22; "GFZ_LN[70]" LOC = A22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = P18; "GFZ_LN[71]" LOC = P18; "GFZ_LN[71]" LOC = N18; "GFZ_LN[71]" LOC = N1
NET NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = J24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[65]" LOC = G22; "GFZ_LN[65]" LOC = G21; "GFZ_LN[66]" LOC = C21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[68]" LOC = L21; "GFZ_LN[69]" LOC = R21; "GFZ_LN[70]" LOC = R21; "GFZ_LN[71]" LOC = M22; "GFZ_LN[71]" LOC = P18; "GFZ_LN[73]" LOC = R18;
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = H21; "GF2_LN[69]" LOC = H22; "GF2_LN[70]" LOC = AB22; "GF2_LN[70]" LOC = B22; "GF2_LN[70]" LOC = D18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = U26; "GFZ_LN[63]" LOC = H21; "GFZ_LN[63]" LOC = H21; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[68]" LOC = J22; "GFZ_LN[69]" LOC = A22; "GFZ_LN[70]" LOC = A22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = J20; "GFZ_LN[71]" LOC = J20; "GFZ_LN[71]" LOC = H22; "GFZ_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET NET NET NET NET NET NET NET NET NET	"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = D22; "GFZ_LN[61]" LOC = U26; "GFZ_LN[61]" LOC = U26; "GFZ_LN[63]" LOC = H21; "GFZ_LN[66]" LOC = H21; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = K21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[68]" LOC = L21; "GFZ_LN[68]" LOC = R21; "GFZ_LN[68]" LOC = R21; "GFZ_LN[70]" LOC = R22; "GFZ_LN[71]" LOC = H22; "GFZ_LN[71]" LOC = H22; "GFZ_LN[72]" LOC = P18; "GFZ_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = L22; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = R22; "GF2_LN[70]" LOC = R22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = R22; "GF2_LN[71]" LOC = R22; "GF2_LN[71]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V3;
NET NET NET NET NET NET NET NET NET NET	<pre>"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = U24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[63]" LOC = J24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[63]" LOC = H21; "GFZ_LN[61]" LOC = G21; "GFZ_LN[61]" LOC = L21; "GFZ_LN[61]" LOC = L21; "GFZ_LN[63]" LOC = J22; "GFZ_LN[63]" LOC = J22; "GFZ_LN[63]" LOC = A22; "GFZ_LN[70]" LOC = A22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = P18; "GFZ_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GFZ_RP[1]" LOC = V4; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = V3;</pre>
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = D22; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[69]" LOC = R21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = AB22; "GF2_LN[71]" LOC = D12; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L4:</pre>
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = C21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = A22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H23; "GF2_LN[71]" LOC = H3; "GF2_LN[73]" LOC = N18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET NET NET NET NET NET NET NET NET NET NET	<pre>"GFZ_LN[58]" LOC = E24; "GFZ_LN[59]" LOC = D22; "GFZ_LN[60]" LOC = U22; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[63]" LOC = H21; "GFZ_LN[63]" LOC = G22; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[69]" LOC = R21; "GFZ_LN[69]" LOC = R21; "GFZ_LN[70]" LOC = R22; "GFZ_LN[71]" LOC = N22; "GFZ_LN[71]" LOC = P18; "GFZ_LN[73]" LOC = P18; "GFZ_LN[73]" LOC = N18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GFZ_RP[1]" LOC = V4; "GFZ_RP[1]" LOC = V4; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = U2;</pre>
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = C24; "GF2_LN[60] " LOC = C24; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = J24; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = H21; "GF2_LN[66]" LOC = C21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[68]" LOC = J22; "GF2_LN[68]" LOC = J22; "GF2_LN[70]" LOC = AB22; "GF2_LN[71]" LOC = AB22; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 </pre>
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = C21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = A22; "GF2_LN[71]" LOC = A22; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45
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NET NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = D22; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[65]" LOC = G21; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = K21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[71]" LOC = L21; "GF2_LN[71]" LOC = L21; "GF2_LN[71]" LOC = L22; "GF2_LN[71]" LOC = M22; "GF2_LN[71]" LOC = M22; "GF2_LN[72]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L4; "GF2_RP[6]" LOC = M3; "GF2_RP[6]" LOC = M3; "GF2_RP[8]" LOC = N2; "GF2_RP[8]" LOC = N2; "GF2_RP[8]" LOC = N4;</pre>
NET	<pre>"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = R21; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H23; "GF2_LN[71]" LOC = H33; "GF2_LN[72]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[2]" LOC = V3; "GF2_RP[2]" LOC = V3; "GF2_RP[2]" LOC = U2; "GF2_RP[2]" LOC = L2; "GF2_RP[2]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = V3; "GF2_RP[6]" LOC = N2; "GF2_RP[9]" LOC = N3; "GF2_RP[9]" LOC = N4; "GF2_RP[9]" LOC = N4; "GF2_RP[9]" LOC = N4;</pre>
NET NET NET NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = G22; "GF2_LN[66]" LOC = G22; "GF2_LN[66]" LOC = K21; "GF2_LN[67]" LOC = L21; "GF2_LN[67]" LOC = R21; "GF2_LN[67]" LOC = R21; "GF2_LN[70]" LOC = R22; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_LN[73]" LOC = V3; "GF2_RP[2]" LOC = V3; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = V3; "GF2_RP[6]" LOC = N4; "GF2_RP[10]" LOC = N4; "GF2_RP[10]" LOC = N4; "GF2_RP[10]" LOC = R4; "GF2_RP[10]" LOC = R4;</pre>
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = D22; "GF2_LN[60]" LOC = D22; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[65]" LOC = G21; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = K21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[71]" LOC = L22; "GF2_LN[71]" LOC = L22; "GF2_LN[71]" LOC = M22; "GF2_LN[71]" LOC = M22; "GF2_LN[72]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = M3; "GF2_RP[6]" LOC = M3; "GF2_RP[6]" LOC = N2; "GF2_RP[6]" LOC = N2; "GF2_RP[6]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = P7;</pre>
NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G21; "GF2_LN[66]" LOC = C21; "GF2_LN[66]" LOC = L21; "GF2_LN[67]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = R21; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H22; "GF2_LN[71]" LOC = H23; "GF2_LN[71]" LOC = H18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = J24; "GF2_LN[63]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = R21; "GF2_LN[61]" LOC = R21; "GF2_LN[70]" LOC = R22; "GF2_LN[70]" LOC = N22; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = V14; "GF2_LN[73]" LOC = N18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V3; "GF2_RP[2]" LOC = V3; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = N3; "GF2_RP[6]" LOC = N4; "GF2_RP[6]" LOC = N4; "GF2_RP[1]" LOC = N5; "GF2_RP[1]" LOC = N2; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_R
NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = D22; "GF2_LN[60]" LOC = D22; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = J24; "GF2_LN[63]" LOC = H21; "GF2_LN[65]" LOC = G21; "GF2_LN[66]" LOC = K21; "GF2_LN[66]" LOC = K21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[68]" LOC = L21; "GF2_LN[71]" LOC = L21; "GF2_LN[71]" LOC = L22; "GF2_LN[71]" LOC = M22; "GF2_LN[71]" LOC = M22; "GF2_LN[71]" LOC = P18; "GF2_LN[72]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = L4; "GF2_RP[1]" LOC = L4; "GF2_RP[6]" LOC = M3; "GF2_RP[6]" LOC = N4; "GF2_RP[6]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R7; "GF2_RP[1]" LOC = R7; "GF2_RP[1]" LOC = R7;</pre>
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = G21; "GF2_LN[61]" LOC = G21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = A22; "GF2_LN[71]" LOC = A22; "GF2_LN[71]" LOC = H21; "GF2_LN[71]" LOC = H18; "GF2_LN[71]" LOC = H18; "GF2_LN[72]" LOC = V13; "GF2_LN[73]" LOC = V14; "GF2_RP[1]" LOC = V3; "GF2_RP[2]" LOC = U2; "GF2_RP[2]" LOC = U2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[1]" LOC = U3; "GF2_RP[1]" LOC = U4; "GF2_RP[3]" LOC = U2; "GF2_RP[6]" LOC = V3; "GF2_RP[6]" LOC = N3; "GF2_RP[6]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P7; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = P10: "GF2_RP[1]" LOC = P10:
NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = G21; "GF2_LN[66]" LOC = G21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = L21; "GF2_LN[66]" LOC = R21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = R22; "GF2_LN[70]" LOC = N22; "GF2_LN[71]" LOC = N3; "GF2_LN[73]" LOC = N18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[2]" LOC = V3; "GF2_RP[2]" LOC = V3; "GF2_RP[3]" LOC = L2; "GF2_RP[5]" LOC = M3; "GF2_RP[5]" LOC = M3; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = N4; "GF2_RP[6]" LOC = N4; "GF2_RP[6]" LOC = N7; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = R3; "GF2_RP[1]" LOC = R3; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R1; "GF2_RP[1]" LOC = R1; "GF2_RP[1
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = C24; "GF2_LN[60]" LOC = C24; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U26; "GF2_LN[63]" LOC = H21; "GF2_LN[64]" LOC = G22; "GF2_LN[66]" LOC = G21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = R21; "GF2_LN[70]" LOC = R21; "GF2_LN[71]" LOC = N22; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[1]" LOC = V3; "GF2_RP[1]" LOC = V3; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = U3; "GF2_RP[3]" LOC = N3; "GF2_RP[1]" LOC = V4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = N7; "GF2_RP[1]" LOC = N7; "GF2_RP[13]" LOC = R1; "GF2_RP[13]" LOC = R3; "GF2_RP[14]" LOC = R3; "GF2_RP[16]" LOC = N3; "GF2_RP[16]" LOC = N3; "GF2_RP[17]" LOC = R1; "GF2_RP[17]" LOC = R1; "GF2_RP[18]" LOC = R3; "GF2_RP[16]" LOC = R3; "GF2_RP[16]" LOC = R3; "GF2_RP[16]" LOC = R3; "GF2_RP[16]" LOC = R1; "GF2_RP[16]" LOC = R1; "GF2_RP</pre>
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = D22; "GF2_LN[60]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[61]" LOC = G21; "GF2_LN[61]" LOC = G21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[69]" LOC = L21; "GF2_LN[69]" LOC = A22; "GF2_LN[71]" LOC = A22; "GF2_LN[71]" LOC = A22; "GF2_LN[71]" LOC = H18; "GF2_LN[71]" LOC = H18; "GF2_LN[71]" LOC = R18; "GF2_LN[72]" LOC = V3; "GF2_LN[73]" LOC = V3; "GF2_RP[1]" LOC = V3; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = U3; "GF2_RP[1]" LOC = L2; "GF2_RP[1]" LOC = N3; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = N2; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = N4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P1; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = P3; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = P10; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R4; "GF2_RP[1]" LOC = R5; "GF2_RP[1]" LOC = R5; "GF2_RP[1]"</pre>
NET NET NET NET NET NET NET NET NET NET	"GF2_LN[58]" LOC = E24; "GF2_LN[59]" LOC = D22; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[61]" LOC = G21; "GF2_LN[61]" LOC = G21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[69]" LOC = R21; "GF2_LN[70]" LOC = R21; "GF2_LN[71]" LOC = N22; "GF2_LN[71]" LOC = N22; "GF2_LN[71]" LOC = N2; "GF2_LN[71]" LOC = N18; Z right (Bank 3,4) #56-#73 not connected on LX45
NET NET NET NET NET NET NET NET NET NET	<pre>"GF2_LN[58] " LOC = E24; "GF2_LN[59] " LOC = C24; "GF2_LN[60]" LOC = C24; "GF2_LN[61]" LOC = U26; "GF2_LN[61]" LOC = U24; "GF2_LN[63]" LOC = H21; "GF2_LN[63]" LOC = H21; "GF2_LN[66]" LOC = G22; "GF2_LN[66]" LOC = C21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = L21; "GF2_LN[61]" LOC = R21; "GF2_LN[70]" LOC = R22; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = P18; "GF2_LN[71]" LOC = P18; "GF2_LN[73]" LOC = R18; Z right (Bank 3,4) #56-#73 not connected on LX45 "GF2_RP[2]" LOC = V3; "GF2_RP[2]" LOC = V3; "GF2_RP[2]" LOC = U2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[3]" LOC = L2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = U2; "GF2_RP[6]" LOC = N4; "GF2_RP[6]" LOC = N7; "GF2_RP[6]" LOC = N7; "GF2_RP[10]" LOC = N4; "GF2_RP[10]" LOC = R4; "GF2_RP[10]" LOC = P3; "GF2_RP[11]" LOC = P3; "GF2_RP[16]" LOC = P3; "GF2_RP[16]" LOC = P3; "GF2_RP[16]" LOC = P3; "GF2_RP[16]" LOC = R4; "GF2_RP[16]" LOC = R4; "GF2_RP[16]" LOC = R4; "GF2_RP[16]" LOC = N3; "GF2_RP[16]" LOC = R1; "GF2_RP[16]" LOC = R1; "GF2_RP[16]" LOC = R1; "GF2_RP[16]" LOC = N3; "GF2_RP[16]" LOC = N4; "GF2_RP[16]" LOC = N3; "GF2_RP[16]" LOC = N4; "GF2_RP[16]" LOC = N4; "GF2_R</pre>
NET NET NET NET NET NET NET NET NET NET	<pre>"GFZ_LN[58] " LOC = E24; "GFZ_LN[59] " LOC = D22; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = U24; "GFZ_LN[61]" LOC = G21; "GFZ_LN[63]" LOC = G21; "GFZ_LN[66]" LOC = G21; "GFZ_LN[66]" LOC = C21; "GFZ_LN[66]" LOC = L21; "GFZ_LN[69]" LOC = L21; "GFZ_LN[69]" LOC = L21; "GFZ_LN[71]" LOC = L22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = A22; "GFZ_LN[71]" LOC = N18; "GFZ_LN[71]" LOC = N18; "GFZ_LN[72]" LOC = V18; "GFZ_LN[73]" LOC = V13; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = L2; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = V3; "GFZ_RP[1]" LOC = N3; "GFZ_RP[1]" LOC = N4; "GFZ_RP[1]" LOC = N2; "GFZ_RP[1]" LOC = N2; "GFZ_RP[2]" LOC = N10; "GFZ_RP[2]" LOC = N10; "GFZ_RP[2]" LOC = N</pre>

NET	"GFZ_RP[2	5]"	LOC =	AA7;
NET	"GFZ_RP[2	6]"	LOC =	M6;
NET	"GFZ_RP[2	7]"	LOC =	L7;
NET	"GFZ RP[2	B1"	LOC =	AA4;
NET	"GF7_RP[2	91"	LOC =	AA2:
NET	"GF7 RP[3]	n i "	LOC =	AB3.
NET	"CF7 DD[3	11"	10C -	D5.
NET	GFZ_RF[J.	-] -] #	100 -	204.
NET	"GFZ_RP[3.	2]"	TOC =	AB4;
NET	"GFZ_RP[3:	3]"	LOC =	AC2;
NET	"GFZ_RP[3	4]"	LOC =	AE2;
NET	"GFZ_RP[3	5]"	LOC =	AD3;
NET	"GFZ RP[3	61"	LOC =	W5;
NET	"GFZ_RP[3]	71"	LOC =	W8:
NET	"GFZ RP[3]	81 W	LOC =	AC7.
NET	CDE_DD[3	018	LOC	TO.
NEI	"GFZ_RP[3	9]	LOC =	18;
NET	"GFZ_RP[4]	0]"	LOC =	AA5;
NET	"GFZ_RP[4]	1]"	LOC =	N8;
NET	"GFZ_RP[42	2]"	LOC =	ΗЗ;
NET	"GFZ_RP[4]	3]"	LOC =	J2;
NET	"GFZ RP[4	41"	LOC =	КЗ;
NET	"GFZ_RP[4]	51"	LOC =	U5:
NET	"GFZ RP[4	61 "	LOC =	E4:
NET	"CF7 PD[4]	~ 71 "	10C -	E3.
NET	GFZ_RF[4	/] 01 W	100 -	E.J.
NET	"GFZ_RP[4]	8]	LOC =	EZ;
NET	"GFZ_RP[4]	9]"	LOC =	G2;
NET	"GFZ_RP[5	0]"	LOC =	W2;
NET	"GFZ_RP[5]	1]"	LOC =	C4;
NET	"GFZ_RP[5:	2]"	LOC =	D3;
NET	"GFZ_RP[5]	31"	LOC =	C2:
NET	"GFZ RP[5	41 "	LOC =	B2.
NET	CDR_DD[5	-1	LOC	N2.
NEI	"GF 4_RP [5:	5]	TOC =	13;
NET	"GFZ_RP[5	6] "	LOC =	H8;
NET	"GFZ_RP[5]	7]"	LOC =	J7;
NET	"GFZ_RP[5	B]"	LOC =	W9;
NET	"GFZ_RP[5	9]"	LOC =	K10;
NET	"GFZ RP[6	01"	LOC =	J10;
NET	"GFZ_RP[6]	11"	LOC =	R2:
NET	"CF7 PD[6	- 1 2 1 II	10C -	x7.
NET	GFZ_RF[0.	-] -] #	100 -	D10
NET	"GFZ_RP[6.	3]"	LOC =	RIU;
NET	"GFZ_RP[6	4]"	LOC =	V7;
NET	"GFZ_RP[6	5]"	LOC =	T10;
NET	"GFZ_RP[6	6]"	LOC =	K5;
NET	"GFZ RP[6	71"	LOC =	J4;
NET	"GFZ_RP[6]	81 "	LOC =	К8:
NET	"GFZ RP[6	9] W	LOC =	v9.
NET	GFZ_RF[0	2] 01 W	100 -	1.J.
NEI	"GFZ_RP[/	J]	TOC =	WIU;
NET	"GFZ_RP[/.	1]"	LOC =	G6;
NET	"GFZ_RP[7:	2]"	LOC =	G4;
NET	"GFZ_RP[7]	3]"	LOC =	F5;
NET NET	"GFZ_RP[7: "GFZ_RP[7:	3]" 4]"	LOC = LOC =	F5; H6;
NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7:	3]" 4]" 5]"	LOC = LOC = LOC =	F5; H6; AA8;
NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7:	3]" 4]" 5]"	LOC = LOC =	F5; H6; AA8;
NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7:	3]" 4]" 5]"]" I	LOC = LOC = LOC =	F5; H6; AA8; I3;
NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1]	3]" 4]" 5]"]" I	LOC = LOC = LOC = V LOC = V	F5; H6; AA8; I3;
NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1]	3]" 4]" 5]"]" I]" I	LOC = V LOC = V LOC = V	F5; H6; AA8; N3; N1;
NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[2]	3]" 4]" 5]"]" I]" I	LOC = I LOC = I LOC = I LOC = I	F5; H6; AA8; V3; V1; L1;
NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[3] "GFZ_RN[3]	3]" 4]" 5]"]" I]" I]" I	IOC = I $IOC = I$ $IOC = I$ $IOC = I$	F5; H6; AA8; V3; V1; L1; L3;
NET NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[2] "GFZ_RN[3] "GFZ_RN[4] "GFZ_RN[5]	3]" 4]" 5]"]" I]" I]" I]" I	POC = P $POC = P$	F5; H6; AA8; V3; V1; L1; L3; V1;
NET NET NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[2] "GFZ_RN[3] "GFZ_RN[4] "GFZ_RN[5] "GFZ_RN[6]	3]" 4]" 5]"]" I]" I]" I]" I]" I	LOC = $LOC =$ $LOC = V$	F5; H6; AA8; N3; X1; S1; S1; S1; J1; J1;
NET NET NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1" "GFZ_RN[2" "GFZ_RN[2" "GFZ_RN[3" "GFZ_RN[4" "GFZ_RN[5" "GFZ_RN[6" "GFZ_RN[7]	3]" 4]" 5]"]" I]" I]" I]" I]" I	LOC = LOC = LOC = LOC = V LOC	F5; H6; AA8; N3; N1; L1; L1; L1; J1; J1; J3;
NET NET NET NET NET NET NET NET NET	"GFZ_RP[7] "GFZ_RP[7] "GFZ_RP[7] "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[3] "GFZ_RN[4] "GFZ_RN[5] "GFZ_RN[6] "GFZ_RN[7] "GFZ_RN[7]	3]" 4]" 5]"]" I]" I]" I]" I]" I]" I	LOC = LOC = LOC = LOC = V LOC	F5; H6; AA8; V3; V1; L1; L3; V1; J1; J1; J3; V1; V1;
NET NET NET NET NET NET NET NET NET	"GFZ_RP[7] "GFZ_RP[7] "GFZ_RP[7] "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[2] "GFZ_RN[3] "GFZ_RN[4] "GFZ_RN[5] "GFZ_RN[6] "GFZ_RN[6] "GFZ_RN[8] "GFZ_RN[8]	3]" 4]" 5]"]" I]" I]" I]" I]" I]" I	LOC = LOC = LOC = LOC = V LOC	F5; H6; AA8; V3; V1; S3; V1; S3; V1; J1; J3; V1; V6;
NET NET NET NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1] "GFZ_RN[2] "GFZ_RN[4] "GFZ_RN[4] "GFZ_RN[6] "GFZ_RN[7] "GFZ_RN[8] "GFZ_RN[8] "GFZ_RN[9]	3]" 4]" 5]" 1" I 1" I 1" I 1" I 1" I 1" I 1" I 1" I 1" I 1" I	LOC = LOC =	F5; H6; AA8; U3; U1; L1; L1; U1; U1; U1; U1; U1; U1; V1; V1; V1; V1; V1; V1; V1; V1; V1; V
NET NET NET NET NET NET NET NET NET	"GFZ_RP[7: "GFZ_RP[7: "GFZ_RP[7: "GFZ_RN[1" "GFZ_RN[2" "GFZ_RN[3" "GFZ_RN[4" "GFZ_RN[6" "GFZ_RN[6" "GFZ_RN[7" "GFZ_RN[8" "GFZ_RN[9" "GFZ_RN[1]"	3]" 4]" 5]"]" I]" I]" I]" I]" I]" I]" I]"	LOC = LOC = L	F5; H6; AA8; N3; /1; .1; .3; H1; J3; H1; J3; H1; N3; N3; P3.
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NET "GFZ RN[4	
	19] LOC = C1.
NET CET DN	[] LOC - U1,
NEI GFZ_RN[.	11 LOC - WI;
NEI "GFZ_RN[:	DI]" LUC = C3;
NET "GFZ_RN[5	52]" LOC = DI;
NET "GFZ_RN[5	53]" LOC = C1;
NET "GFZ_RN[5	54]" LOC = B1;
NET "GFZ_RN[5	55]" LOC = Y1;
NET "GFZ_RN[5	56]" LOC = G7;
NET "GFZ_RN[5	57]" LOC = H7;
NET "GFZ_RN[5	58]" LOC = V8;
NET "GFZ_RN[5	59]" LOC = L9;
NET "GFZ RN[6	50]" LOC = K9;
NET "GFZ RN[6	51]" LOC = R1;
NET "GEZ RN[6	2]" LOC = K6:
NET "GEZ BN[A	31" IOC = T9
NET "GEZ RN[6	541" LOC = V6
NET "GEZ RN[6	51" LOC = 119.
NET "CE7 DN[4	55] LOC - 15;
NEI GFZ_RN(C	50] LOC = 55;
NEI "GFZ_RN[6	5/]" LOC = J3;
NET "GFZ_RN[6	58]" LOC = L8;
NET "GFZ_RN[6	99]" LOC = Y8;
NET "GFZ_RN[]	(0]" LOC = V10;
NET "GFZ_RN[7	1]" LOC = G5;
NET "GFZ_RN[7	2]" LOC = G3;
NET "GFZ_RN[7	3]" LOC = E5;
NET "GFZ_RN[7	4]" LOC = H5;
NET "GFZ_RN[7	[5]" LOC = AB8;
#FMC LPC (Bar	nk 0)
#	
NET "FMC_CLK	_M2C_P[0]" LOC = B14;
NET "FMC CLK	M2C_N[0]" LOC = A14:
NET "FMC CLK	$M_{2C}^{(1)} P[1] = L_{0C}^{(1)} = C_{13}^{(1)}$
NET "FMC CLK	$M_{2C} N[1] = LOC = A13;$
NET "FMC LA D	P[0]" LOC = B12:
NET "FMC IA N	I[0] " IOC = 12.
NET FINC IN I	111 IOC - R9.
NET FMC IA N	[1] LOC - DO,
NET PMC_LA_P	([1] LOC - A0;
NEI "FMC_LA_F	[2]" LOC = CII;
NET "FMC_LA_P	I[Z]" LOC = AII;
NET "FMC_LA_F	[3]" LOC = C9;
NET "FMC_LA_N	[3]" LOC = A9;
NET "FMC_LA_F	P[4] LOC = C7;
NET "FMC_LA_N	1[4]" LOC = A7;
NET "FMC_LA_F	P[5]" LOC = C5;
NET "FMC_LA_N	1[5]" LOC = A5;
NET "FMC_LA_F	P[6]" LOC = D6;
NET "FMC_LA_N	1[6]" LOC = C6;
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NET "FMC_LA_P NET "FMC LA N	1[7]" LOC = A4;
NET "FMC_LA_E NET "FMC_LA_N NET "FMC_LA_E	[7] LOC = B4; [7]" LOC = A4; [8]" LOC = B6;
NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_N	[7] LOC = B4; [7]" LOC = A4; P[8]" LOC = B6; I[8]" LOC = A6;
NET "FMC_LA_F NET "FMC_LA_M NET "FMC_LA_F NET "FMC_LA_M NET "FMC_LA_M	I[7] LOC = B4; I[7]" LOC = A4; P[8]" LOC = B6; I[8]" LOC = A6; P[9]" LOC = D21;
NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_F	[7] = 100 - 54; [7] = 100 - 54; [8] = 100 - 56; [8] = 100 - 56; [9] = 100 - 521; [9] = 100 - 520;
NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_P NET "FMC_LA_F NET "FMC_LA_P NET "FMC_LA_N	[7] LOC = A4; [[3] LOC = A4; [[8] LOC = B6; [[8] LOC = A6; [9] LOC = D21; [19] LOC = D21; [10] LOC = D16;
NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_E NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F	<pre>[7] LOC = B4; [7]" LOC = A4; [8]" LOC = B6; [8]" LOC = A6; [9]" LOC = D21; [9]" LOC = C20; [10]" LOC = B16; [10]" LOC = B16;</pre>
NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_N	<pre>[1] LOC = A4; [1]" LOC = A4; [2]" LOC = B6; [3]" LOC = B6; [3]" LOC = D21; [3]" LOC = D21; [3]" LOC = D21; [3]" LOC = B16; [10]" LOC = B16; [3]" LOC = D36;</pre>
NET "FMC_LA_H NET "FMC_LA_H NET "FMC_LA_H NET "FMC_LA_L NET "FMC_LA_L NET "FMC_LA_L NET "FMC_LA_L NET "FMC_LA_L NET "FMC_LA_L	<pre>[1] LOC = A4; [1]" LOC = A4; [8]" LOC = B6; [8]" LOC = A6; [9]" LOC = D21; [9]" LOC = D21; [10]" LOC = D16; [10]" LOC = A16; [11]" LOC = D18;</pre>
NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F	[1] = 10C = A4; $[2] = 10C = A6;$ $[3] = 10C = B6;$ $[3] = 10C = B6;$ $[3] = 10C = D21;$ $[3] = 10C = C20;$ $[10] = 10C = C20;$ $[10] = 10C = B16;$ $[11] = 10C = D18;$ $[11] = 10C = C18;$ $[11] = 10C = C18;$
NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N	<pre>[1] LOC = A4; [1]" LOC = A4; [2]" LOC = B6; [3]" LOC = B6; [3]" LOC = D21; [3]" LOC = D21; [3]" LOC = D21; [3]" LOC = B16; [1]" LOC = B16; [11]" LOC = A16; [11]" LOC = C18; [12]" LOC = C17; [3]" LOC = C17;</pre>
NET "FMC_LA_N NET "FMC_LA_N	<pre>[1] LOC = A4; [1]" LOC = A4; [8]" LOC = B6; [8]" LOC = D21; [9]" LOC = D21; [10]" LOC = D21; [10]" LOC = D16; [11]" LOC = A16; [11]" LOC = C18; [11]" LOC = C17; [12]" LOC = A17;</pre>
NET "PMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_M NET "FMC_LA_M	<pre>[1] LOC = A4; [1]" LOC = A4; [2]" LOC = B6; [3]" LOC = B6; [3]" LOC = D21; [4]" LOC = D21; [4]" LOC = D21; [4]" LOC = D16; [4]" LOC = A16; [4]" LOC = C18; [4]" LOC = C17; [4]" LOC = A17; [4] ON LV45</pre>
NET "PMC_LA_E NET "PMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_MET "FMC_LA_MET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E	<pre>[1] LOC = A4; [1]" LOC = A4; [2]" LOC = B6; [3]" LOC = D6; [3]" LOC = D21; [3]" LOC = D21; [4]0" LOC = D16; [4]0" LOC = A16; [4]11" LOC = D18; [4]11" LOC = C18; [4]21" LOC = C17; [4]21" LOC = C17; [4]21" LOC = A17; [4]31" LOC = B10; [4]31" LOC = B10;</pre>
NET "PMC_LA_E NET "FMC_LA_E NET "FMC_LA_M NET "FMC_LA_M	<pre>[1] LOC = A4; [1]" LOC = A4; [8]" LOC = B6; [8]" LOC = D21; [9]" LOC = D21; [10]" LOC = D21; [10]" LOC = D16; [11]" LOC = D18; [11]" LOC = C18; [12]" LOC = C17; [12]" LOC = A17; [13]" LOC = B10; [13]" LOC = B10;</pre>
NET "PMC_LA_I NET "PMC_LA_I NET "PMC_LA_N NET "PMC_LA_N	[1] LOC = A4; $[[7]" LOC = A4;$ $[8]" LOC = B6;$ $[[9]" LOC = D21;$ $[9]" LOC = C20;$ $[10]" LOC = B16;$ $[10]" LOC = A16;$ $[[11]" LOC = C18;$ $[[11]" LOC = C17;$ $[12]" LOC = C17;$ $[12]" LOC = A17;$ $[21]" LOC = B10;$ $[[13]" LOC = B10;$ $[[13]" LOC = B1;$ $[14]" LOC = G8;$
NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E	<pre>[1] LOC = A4; [1] LOC = A4; [2] LOC = A6; [3] LOC = D6; [3] LOC = D21; [4] LOC = D21; [4] LOC = D16; [4] LOC = D16; [4] LOC = D18; [4] LOC = D18; [4] LOC = C18; [4] LOC = C17; [4] LOC = A10; [4] LOC = A10; [4] LOC = A10; [4] LOC = G8; [4] LOC = C7; [4] LOC = C7;</pre>
NET "PMC_LA_N NET "FMC_LA_N NET "FMC_LA_F NET "FMC_LA_F NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_S NET "FMC_LA_S NET "FMC_LA_S NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N NET "FMC_LA_N	<pre>[1] LOC = A4; [1]" LOC = A4; [8]" LOC = B6; [8]" LOC = D21; [9]" LOC = D21; [10]" LOC = D16; [10]" LOC = A16; [11]" LOC = A16; [11]" LOC = A16; [11]" LOC = C17; [12]" LOC = C17; [12]" LOC = C17; [12]" LOC = A17; [13]" LOC = B10; [13]" LOC = B10; [14]" LOC = F7; [15]" LOC = F17;</pre>
NET "PMC_LA_I NET "FMC_LA_I NET "FMC_LA_M NET "FMC_LA_M	<pre>[1] LOC = A4; [2] LOC = A4; [2] LOC = A6; [3] LOC = A6; [4] LOC = D21; [4] LOC = D21; [4] LOC = D21; [4] LOC = D16; [4] LOC = A16; [4] LOC = C17; [4] LOC = C17; [4] LOC = C17; [4] LOC = A17; [4] LOC = B10; [4] LOC = B10; [4] LOC = G8; [4] LOC = G8; [4] LOC = F7; [4] LOC = F7; [4] LOC = F7; [4] LOC = E17;</pre>
NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M	[1] LOC = A4; $[[7]" LOC = A4;$ $[8]" LOC = B6;$ $[[8]" LOC = D21;$ $[9]" LOC = D21;$ $[9]" LOC = C20;$ $[10]" LOC = B16;$ $[11]" LOC = D18;$ $[[11]" LOC = C18;$ $[11]" LOC = C18;$ $[11]" LOC = A17;$ $[12]" LOC = A17;$ $[12]" LOC = A17;$ $[13]" LOC = B10;$ $[[13]" LOC = B10;$ $[[14]" LOC = G8;$ $[[14]" LOC = G8;$ $[[14]" LOC = F7;$ $[[15]" LOC = F17;$ $[[15]" LOC = H17;$ $[[16]" LOC = J15;$ $[16]" LOC = J15;$
NET "FMC_LA_ NET "FMC_LA_	<pre>[1] LOC = A4; [1]" LOC = A4; [8]" LOC = B6; [8]" LOC = D21; [9]" LOC = D21; [10]" LOC = D21; [10]" LOC = D16; [11]" LOC = D16; [11]" LOC = C18; [11]" LOC = C18; [12]" LOC = C17; [12]" LOC = A17; [13]" LOC = B10; [13]" LOC = B10; [13]" LOC = B10; [14]" LOC = F17; [15]" LOC = F17; [15]" LOC = J15; [16]" LOC = J15;</pre>
NET "PMC_LA_I NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M	[1] LOC = A4; $[[7]" LOC = A4;$ $[[8]" LOC = A6;$ $[[9]" LOC = A6;$ $[[9]" LOC = A2;$ $[10]" LOC = D21;$ $[10]" LOC = D16;$ $[11]" LOC = D18;$ $[11]" LOC = D18;$ $[11]" LOC = C17;$ $[11]" LOC = C17;$ $[11]" LOC = A17;$ $[12]" LOC = A10;$ $[13]" LOC = B10;$ $[13]" LOC = B10;$ $[14]" LOC = G8;$ $[14]" LOC = F7;$ $[15]" LOC = E17;$ $[15]" LOC = H15;$ $[16]" LOC = H15;$ $[17]" LOC = F16;$
NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_E NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M NET "PMC_LA_M	[1] LOC = A4; $[[7]" LOC = A4;$ $[8]" LOC = B6;$ $[[8]" LOC = D21;$ $[9]" LOC = D21;$ $[9]" LOC = D21;$ $[10]" LOC = B16;$ $[11]" LOC = D18;$ $[11]" LOC = C18;$ $[11]" LOC = C17;$ $[12]" LOC = A17;$ $Bd on LX45$ $[13]" LOC = A10;$ $[13]" LOC = A10;$ $[14]" LOC = F1;$ $[14]" LOC = F7;$ $[15]" LOC = F17;$ $[15]" LOC = J15;$ $[16]" LOC = F16;$ $[17]" LOC = E16;$
NET "PMC_LA_N NET "FMC_LA_N NET "FMC_LA_E NET "FMC_LA_E NE	[1] LOC = A4; $[[7]" LOC = A6;$ $[[8]" LOC = B6;$ $[[9]" LOC = D21;$ $[[9]" LOC = D21;$ $[[10]" LOC = D16;$ $[[10]" LOC = A16;$ $P[11]" LOC = A16;$ $P[11]" LOC = C18;$ $[[11]" LOC = C18;$ $[[12]" LOC = C17;$ $[[12]" LOC = A17;$ $Id on LX45$ $[[13]" LOC = B10;$
NET "PMC_LA_I NET "PMC_LA_I NET "PMC_LA_N NET "PMC_LA_N NET "PMC_LA_N NET "PMC_LA_N NET "PMC_LA_N NET "PMC_LA_I NET "PMC_LA_N NET "PMC_LA_N	[1] LOC = A4; $[2] LOC = A4;$ $[2] LOC = A6;$ $[2] LOC = A6;$ $[2] LOC = D21;$ $[3] LOC = D21;$ $[3] LOC = D21;$ $[1] LOC = D16;$ $[11] LOC = D16;$ $[11] LOC = D18;$ $[11] LOC = C17;$ $[11] LOC = C17;$ $[11] LOC = A17;$ $[2] LOC = B10;$ $[13] LOC = B10;$ $[14] LOC = F1;$ $[15] LOC = H15;$ $[17] LOC = H16;$ $[17] LOC = E16;$ $[18] LOC = F9;$
NET "PMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E NET "FMC_LA_E	[1] = 100 - 84; $[(7)] = 100 - 84;$ $[(8)] = 100 - 86;$ $[(8)] = 100 - 86;$ $[(9)] = 100 - 816;$ $[(10)] = 100 - 816;$ $[(11)] = 100 - 816;$ $[(11)] = 100 - 816;$ $[(11)] = 100 - 816;$ $[(11)] = 100 - 816;$ $[(11)] = 100 - 817;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 810;$ $[(11)] = 100 - 80;$ $[(11)] = 1$
NET "PMC_LA_ NET "PMC_LA_	[1] LOC = A4; $[[7]" LOC = A6;$ $[[8]" LOC = B6;$ $[[9]" LOC = D21;$ $[[9]" LOC = D21;$ $[[10]" LOC = D16;$ $[[10]" LOC = D16;$ $[[11]" LOC = D18;$ $[[11]" LOC = C18;$ $[[11]" LOC = C18;$ $[[12]" LOC = A17;$ $[12]" LOC = A10;$ $[[13]" LOC = B10;$ $[[13]" LOC = F9;$ $[[13]" LOC = F9;$ $[[13]" LOC = D10;$
NET "PMC_LA_I NET "PMC_LA_I	[17] = LOC = A4; $[[7]" LOC = A4;$ $[2[8]" LOC = A6;$ $[19]" LOC = A6;$ $[19]" LOC = D21;$ $[19]" LOC = D21;$ $[10]" LOC = D16;$ $[11]" LOC = D16;$ $[11]" LOC = C17;$ $[11]" LOC = C17;$ $[11]" LOC = C17;$ $[12]" LOC = A17;$ $[13]" LOC = A10;$ $[13]" LOC = A10;$ $[13]" LOC = B10;$ $[13]" LOC = B10;$ $[13]" LOC = B10;$ $[13]" LOC = F1;$ $[15]" LOC = F1;$ $[15]" LOC = F1;$ $[16]" LOC = H15;$ $[17]" LOC = F16;$ $[17]" LOC = F16;$ $[17]" LOC = F9;$ $[18]" LOC = F9;$ $[18]" LOC = F9;$ $[19]" LOC = D10;$ $[19]" LOC = C10;$
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[7] LOC = A4;$ $[[8] LOC = B6;$ $[[8] LOC = D21;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = A16;$ $[[11] LOC = A16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = A10;$ $[[13] LOC = A10;$ $[[13] LOC = B10;$ $[[14] LOC = B10;$ $[[16] LOC = H15;$ $[[16] LOC = H15;$ $[[17] LOC = E16;$ $[18] LOC = E9;$ $[[18] LOC = E9;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = B10;$ $[[10] LO$
NET "PMC_LA_ NET "PMC_LA_	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[10] LOC = D16;$ $[[11] LOC = D18;$ $[[11] LOC = C18;$ $[[11] LOC = C18;$ $[[11] LOC = C18;$ $[[12] LOC = A10;$ $[[13] LOC = B10;$ $[[15] LOC = B11;$ $[[15] LOC = B11;$ $[[16] LOC = B16;$ $[[17] LOC = F16;$ $[[17] LOC = F16;$ $[[18] LOC = B10;$ $[[19] LOC = D10;$ $[[19] LOC = C10;$ $[[20] LOC = E10;$ $[[20] LOC = E1$
NET "PMC_LA_I NET "PMC_LA_I	[17] LOC = A4; $[[7]" LOC = A4;$ $[28]" LOC = B6;$ $[[8]" LOC = D21;$ $[[9]" LOC = D21;$ $[[9]" LOC = D21;$ $[19]" LOC = D16;$ $[11]" LOC = D16;$ $[11]" LOC = D16;$ $[11]" LOC = C17;$ $[11]" LOC = C17;$ $[11]" LOC = A17;$ $[12]" LOC = A17;$ $[12]" LOC = A17;$ $[13]" LOC = B10;$ $[13]" LOC = B10;$ $[13]" LOC = B10;$ $[13]" LOC = F7;$ $[15]" LOC = F7;$ $[15]" LOC = F17;$ $[15]" LOC = F17;$ $[16]" LOC = F16;$ $[17]" LOC = E16;$ $[17]" LOC = E16;$ $[16]" LOC = F9;$ $[18]" LOC = C10;$ $[19]" LOC = C10;$ $[19]" LOC = C10;$ $[19]" LOC = C10;$ $[10]" LOC = E10;$ $[120]" LOC = E10;$ $[120]" LOC = E10;$ $[120]" LOC = D12;$ $[221]" LOC = D12;$
NET "PMC_LA_ NET "PMC_LA_	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = D16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = A10;$ $[[13] LOC = B10;$ $[[13] LOC = D10;$ $[[13] LOC = D10;$ $[[13] LOC = B10;$ $[[13] $
NET "PMC_LA_ NET "PMC_LA_	[1] LOC = A4; $[[0] LOC = A4;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = D16;$ $[[11] LOC = D18;$ $[[11] LOC = C18;$ $[[11] LOC = C18;$ $[[11] LOC = A10;$ $[[12] LOC = A10;$ $[[13] LOC = A10;$ $[[13] LOC = B10;$ $[[16] LOC = B10;$ $[[16] LOC = B10;$ $[[17] LOC = B10;$ $[[17] LOC = B10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = B10;$ $[[20] LOC = B10;$ $[[21] LOC = D12;$ $[[21] LOC = D12;$ $[[21] LOC = D12;$ $[[22] LOC = D12;$ $[[22] $
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[2] LOC = A4;$ $[2] LOC = A6;$ $[3] LOC = D21;$ $[3] LOC = D21;$ $[4] LOC = D21;$ $[4] LOC = D16;$ $[11] LOC = D16;$ $[11] LOC = D16;$ $[11] LOC = C17;$ $[11] LOC = C17;$ $[11] LOC = A17;$ $[12] LOC = A17;$ $[13] LOC = A10;$ $[13] LOC = B10;$ $[14] LOC = F1;$ $[15] LOC = E17;$ $[15] LOC = E17;$ $[16] LOC = E16;$ $[17] LOC = E16;$ $[17] LOC = E16;$ $[16] LOC = E9;$ $[18] LOC = C10;$ $[19] LOC = D10;$ $[19] LOC = D10;$ $[120] LOC = D10;$ $[121] LOC = D12;$ $[121] LOC = C12;$ $[122] LOC = D11;$ $[122] LOC = D11;$ $[122] LOC = E11;$
NET "PMC_LA_ NET "	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = A10;$ $[[13] LOC = B10;$ $[[13] $
NET "PMC_LA.I NET "PMC_LA.E NET "PMC_LA.E NET "PMC_LA.P NET "PMC_LA.P NET "PMC_LA.P NET "PMC_LA.P NET "PMC_LA.E NET "PMC_LA.E	[1] LOC = A4; $[[0] LOC = A4;$ $[[0] LOC = A6;$ $[[0] LOC = D21;$ $[[0] LOC = D21;$ $[[1] LOC = D16;$ $[[1] LOC = D16;$ $[[1] LOC = D18;$ $[[1] LOC = C18;$ $[[1] LOC = C18;$ $[[1] LOC = A17;$ $[[1] LOC = A10;$ $[[1] LOC = B10;$ $[[2] LOC = D10;$ $[[2] LOC = D10;$ $[[2] LOC = D11;$ $[[2] LOC = D11;$ $[[2] LOC = D11;$ $[[2] LOC = D11;$ $[[2] LOC = B1;$ $[[2]$
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[7]" LOC = A4;$ $[[8]" LOC = B6;$ $[[8]" LOC = D21;$ $[[9]" LOC = D21;$ $[[9]" LOC = D21;$ $[[10]" LOC = D16;$ $[[11]" LOC = D18;$ $[[11]" LOC = C17;$ $[[11]" LOC = C17;$ $[[11]" LOC = A16;$ $[[11]" LOC = A17;$ $[12]" LOC = A17;$ $[13]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = B10;$ $[[13]" LOC = C17;$ $[[15]" LOC = F17;$ $[[15]" LOC = H15;$ $[[16]" LOC = H15;$ $[[16]" LOC = H15;$ $[[16]" LOC = H15;$ $[[16]" LOC = H16;$ $[[17]" LOC = E16;$ $[16]" LOC = E10;$ $[[19]" LOC = C10;$ $[[20]" LOC = F10;$ $[[20]" LOC = F10;$ $[[21]" LOC = D12;$ $[[22]" LOC = D11;$ $[[22]" LOC = D1;$ $[[22]" LOC = D1;$ $[[23]" LOC = D8;$ $[[23]" LOC = C1;$ $[[23]" LOC = D8;$ $[[23]" LOC = D8;$ $[[23]" LOC = D8;$ $[[23]" LOC = D8;$ $[[23]" LOC = C1;$ $[[23]" LOC = C1;$ $[[23]" LOC = D8;$ $[[23]"$
NET "PMC_LA. NET "PMC_LA. NE	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = L16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = A10;$ $[[13] LOC = C17;$ $[[13] LOC = C17;$ $[[13] LOC = C17;$ $[[13] LOC = E11;$ $[[14] LOC = E17;$ $[[15] LOC = H15;$ $[[16] LOC = H15;$ $[[17] LOC = E16;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D12;$ $[[22] LOC = D12;$ $[[22] LOC = D11;$ $[[22] LOC = D13;$ $[[22] LOC = D14;$ $[[22] LOC = C14;$ $[[22] LOC = C14;$ $[[22] LOC = D14;$ $[[22] LOC = D14;$ $[[22] LOC = D14;$ $[[22] LOC = C14;$ $[[22] $
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[0] LOC = A4;$ $[[0] LOC = A6;$ $[[0] LOC = D6;$ $[[0] LOC = D21;$ $[[0] LOC = D16;$ $[[1] LOC = A16;$ $[[1] LOC = A16;$ $[[1] LOC = A16;$ $[[1] LOC = C17;$ $[[1] LOC = C18;$ $[[1] LOC = A10;$ $[[1] LOC$
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[7]" LOC = A4;$ $[[8]" LOC = B6;$ $[[8]" LOC = D21;$ $[[9]" LOC = D21;$ $[[9]" LOC = D21;$ $[[10]" LOC = D16;$ $[[11]" LOC = D18;$ $[[11]" LOC = C17;$ $[[11]" LOC = C17;$ $[[11]" LOC = C17;$ $[[12]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = A10;$ $[[13]" LOC = B10;$ $[[14]" LOC = F17;$ $[[15]" LOC = H15;$ $[[16]" LOC = H15;$ $[[16]" LOC = H15;$ $[[16]" LOC = H16;$ $[[17]" LOC = E16;$ $[16]" LOC = B10;$ $[[19]" LOC = D10;$ $[[19]" LOC = D10;$ $[[19]" LOC = C10;$ $[[20]" LOC = F10;$ $[[21]" LOC = D12;$ $[[22]" LOC = D11;$ $[[22]" LOC = D1;$ $[[22]" LOC = D1;$ $[[23]" LOC = C3;$ $[[24]" LOC = C13;$ $[[24]" LOC = C13;$ $[[24]" LOC = C13;$ $[[22]" LOC = C13;$ $[[24]" LOC = C13;$ $[[24]" LOC = C13;$ $[[22]" LOC = C13;$ $[[24]" LOC = C13;$ $[[24]" LOC = C13;$ $[[24]" LOC = C13;$ $[[24]" LOC = C3];$ $[[25]" LOC = C3;$ $[[25]" LOC$
NET "PMC_LA_ NET "	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = L16;$ $[[11] LOC = L17;$ $[[12] LOC = L17;$ $[[13] LOC = L17;$ $[[13] LOC = L17;$ $[[14] LOC = L17;$ $[[15] LOC = L17;$ $[[16] LOC = L17;$ $[[16] LOC = L17;$ $[[16] LOC = L17;$ $[[17] LOC = L17;$ $[[16] LOC = L17;$ $[[17] LOC = L17;$ $[[18] LOC = L17;$ $[[19] LOC = L17;$ $[[19] LOC = L16;$ $[[19] LOC = L16;$ $[[19] LOC = L10;$ $[[19] LOC = L10;$ $[[19] LOC = L10;$ $[[19] LOC = L10;$ $[[22] LOC = L12;$ $[[22] LOC = L12;$ $[[22] LOC = L13;$ $[[22] $
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[0]'' LOC = A4;$ $[[0]'' LOC = A6;$ $[[0]'' LOC = D21;$ $[[0]'' LOC = D21;$ $[[1]'' LOC = D16;$ $[[1]'' LOC = D16;$ $[[1]'' LOC = C17;$ $[[1]'' LOC = C18;$ $[[1]'' LOC = C18;$ $[[1]'' LOC = C18;$ $[[1]'' LOC = A10;$ $[[2]'' LOC = A10;$ $[[2]''$
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[[7] LOC = A4;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = D16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = C17;$ $[[13] LOC = A10;$ $[[13] $
NET "PMC_LA_ NET "	[1] LOC = A4; $[[7] LOC = A4;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = L16;$ $[[11] LOC = C17;$ $[[12] LOC = C18;$ $[[11] LOC = C18;$ $[[11] LOC = A10;$ $[[13] LOC = B10;$ $[[13] LOC = C17;$ $[[13] LOC = C17;$ $[[13] LOC = C17;$ $[[13] LOC = E17;$ $[[14] LOC = E17;$ $[[15] LOC = H15;$ $[[16] LOC = H15;$ $[[17] LOC = E16;$ $[[16] LOC = H15;$ $[[17] LOC = E16;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = C10;$ $[[20] LOC = D10;$ $[[21] LOC = D12;$ $[[21] LOC = D12;$ $[[21] LOC = D13;$ $[[22] LOC = C13;$ $[[22] LOC = H1;$ $[[22] LOC = H1;$ $[[22] LOC = H1;$ $[[22] LOC = H1;$ $[[23] LOC = C13;$ $[[23] LOC = C13;$ $[[23] LOC = C13;$ $[[22] LOC = H1;$ $[[23] LOC = D1;$ $[[23] LOC = D1;$ $[[23] LOC = H1;$ $[[23] LOC =$
NET "PMC_LA.] NET "PMC_LA.]	[17] LOC = A4; $[[7] LOC = A4;$ $[[8] LOC = A6;$ $[[9] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = L17;$ $[[12] LOC = C17;$ $[[12] LOC = C17;$ $[[13] LOC = B10;$ $[[16] LOC = J15;$ $[[16] LOC = H15;$ $[[17] LOC = B16;$ $[[16] LOC = B10;$ $[[17] LOC = B10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[20] LOC = B10;$ $[[21] LOC = C12;$ $[[22] LOC = D11;$ $[[22] LOC = B1;$ $[[22] LOC = C13;$ $[[22] LOC = C14;$ $[[22] LOC = C13;$ $[[22] LOC = C14;$ $[[22] LOC = D13;$ $[[22] LOC = C13;$ $[[22] LOC = D13;$ $[[22] LOC = C13;$ $[[22] LOC = D13;$ $[[22] LOC = C13;$ $[[22] $
NET "PMC_LA. NET "PMC_LA.	[1]
NET "PMC_LA_ NET "	[1] LOC = A4; $[[7] LOC = A6;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[[10] LOC = D16;$ $[[11] LOC = L16;$ $[[11] LOC = L16;$ $[[11] LOC = L17;$ $[[12] LOC = L17;$ $[[13] LOC = L17;$ $[[13] LOC = L17;$ $[[14] LOC = L17;$ $[[15] LOC = L17;$ $[[16] LOC = H15;$ $[[16] LOC = H15;$ $[[17] LOC = L17;$ $[[16] LOC = H15;$ $[[17] LOC = L17;$ $[[18] LOC = L17;$ $[[19] LOC = L16;$ $[[17] LOC = H16;$ $[[19] LOC = L10;$ $[[19] LOC = L10;$ $[[19] LOC = H10;$ $[[19] LOC = L10;$ $[[20] LOC = H10;$ $[[21] LOC = L10;$ $[[21] LOC = L10;$ $[[22] LOC = L10;$ $[[22] LOC = L11;$ $[[22] LOC = L11;$ $[[22] LOC = L12;$ $[[22] LOC = L12;$ $[[22] LOC = H1;$ $[[23] LOC = L13;$ $[[23] LOC = L13;$ $[[23] LOC = L13;$ $[[24] LOC = H1;$ $[[25] LOC = H1;$ $[[25] LOC = H1;$ $[[25] LOC = H1;$ $[[26] LOC = H1;$ $[[26] LOC = H1;$ $[[26] LOC = H1;$ $[[26] LOC = H1;$ $[[28] LOC = H1$
NET "PMC_LA_I NET "PMC_LA_I	[17] LOC = A4; $[[7] LOC = A4;$ $[[8] LOC = A6;$ $[[9] LOC = D21;$ $[[9] LOC = D21;$ $[10] LOC = D16;$ $[[11] LOC = D16;$ $[[11] LOC = D16;$ $[[11] LOC = C17;$ $[[12] LOC = C17;$ $[[12] LOC = A10;$ $[[13] LOC = B10;$ $[[15] LOC = B11;$ $[[15] LOC = B11;$ $[[16] LOC = B16;$ $[[17] LOC = B10;$ $[[16] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[19] LOC = D10;$ $[[20] LOC = B10;$ $[[21] LOC = D11;$ $[[22] LOC = D11;$ $[[22] LOC = D11;$ $[[22] LOC = B1;$ $[[22] LOC = C12;$ $[[22] LOC = C12;$ $[[22] LOC = C12;$ $[[22] LOC = B1;$ $[[22] LOC = B1;$ $[[22] LOC = D1;$ $[[22] LOC = D1;$ $[[22] LOC = D1;$ $[[22] LOC = B1;$ $[[22] LOC = C1;$ $[[22] LOC = C1;$ $[[22] LOC = B1;$ $[[22] LOC = B1$
NET "PMC_LA_I NET "PMC_LA_I	[1] LOC = A4; $[1] LOC = A4;$ $[1] LOC = A6;$ $[10] LOC = D21;$ $[10] LOC = D21;$ $[10] LOC = D16;$ $[11] LOC = D16;$ $[11] LOC = C17;$ $[11] LOC = C17;$ $[11] LOC = C17;$ $[11] LOC = A10;$ $[12] LOC = B10;$ $[12] LOC = A10;$ $[12] LOC =$

NET "FMC_LA_N[30]" LOC = G17; NET "FMC_LA_P[31]" LOC = D16; NET "FMC_LA_N[31]" LOC = C16; NET "FMC_LA_N[32]" LOC = E19; NET "FMC_LA_N[32]" LOC = D19; NET "FMC_LA_N[33]" LOC = F18; NET "FMC_LA_N[33]" LOC = E18;

1.7 Hardware revisions

1.7.1 Revision 13.09

Minor issues have been identified with this revision of the LinoSPAD motherboard.

- For the two LEDs to work the transistors T1 and T2 need to be soldered on their back, such that two pins are exchanged.
- For the FX3 to configure correctly from the flash memory the resistor R40 on the back needs to be removed.
- For the Spartan 6 to use the flash memory a jumper wire has to be soldered from J9 pin 2 to the chip select of the memory, pin 7 of IC4 or the left side of R20.